

Refine Search

Search Results -

Term	Documents
"5926832"	1
5926832S	0
HEAD	671892
HEADS	162586
(("5926832".PN.) AND HEAD).PGPB,USPT.	1
(5926832.PN. AND HEAD).PGPB,USPT.	1

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Database:
 L29

Search History

DATE: Wednesday, April 14, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=PGPB,USPT; PLUR=YES; OP=OR

Hit Count Set Name

result set

<u>L29</u>	5926832.pn. and head	1	<u>L29</u>
<u>L28</u>	L27 and (pc or counter)	1	<u>L28</u>
<u>L27</u>	6182203.pn. and variable	1	<u>L27</u>
<u>L26</u>	l17 and variable	1	<u>L26</u>
<u>L25</u>	vliw near30 L22	0	<u>L25</u>
<u>L24</u>	vliw near15 L22	0	<u>L24</u>
<u>L23</u>	vliw and L22	62	<u>L23</u>
<u>L22</u>	queue\$1 near5 point\$3	5425	<u>L22</u>
<u>L21</u>	L20 and vliw	1	<u>L21</u>

<u>L20</u>	register near3 queu\$3 near point\$3	168	<u>L20</u>
<u>L19</u>	l17 and register\$1	1	<u>L19</u>
<u>L18</u>	L17 and queu\$3	0	<u>L18</u>
<u>L17</u>	execute near2 packet\$1 and l16	1	<u>L17</u>
<u>L16</u>	6182203.pn. and fetch\$3 near2 packet\$1	1	<u>L16</u>
<u>L15</u>	6182203.pn. and fectch\$3 near2 packet\$1	0	<u>L15</u>
<u>L14</u>	6182203.pn. and fectch near2 packet\$1	0	<u>L14</u>
<u>L13</u>	L12 and (queu\$3)	15	<u>L13</u>
<u>L12</u>	L11 and l7	58	<u>L12</u>
<u>L11</u>	(712/2-300)! [CCLS]	9639	<u>L11</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L10</u>	select\$6 near5 execut\$3 near12 packet\$1 and vliw	10	<u>L10</u>
<u>L9</u>	L8 and (identif\$6 or detect\$3 or determin\$6)	17	<u>L9</u>
<u>L8</u>	L7 and (prefetch\$3 or fetch\$3) and queu\$3	19	<u>L8</u>
<u>L7</u>	vliw near6 packet\$1	80	<u>L7</u>
<u>L6</u>	L4 and (prefetch\$3 or fetch\$3) and queu\$3	6	<u>L6</u>
<u>L5</u>	L4 (prefetch\$3 or fetch\$3) and queu\$3	10809	<u>L5</u>
<u>L4</u>	(vliw) near9 loop\$4	53	<u>L4</u>
<u>L3</u>	L1 and (prefetch\$3 or fetch\$3) and queu\$3	6	<u>L3</u>
<u>L2</u>	L1 and fetch\$3 and queu\$3	6	<u>L2</u>
<u>L1</u>	(vliw) near5 loop\$4	44	<u>L1</u>

END OF SEARCH HISTORY

Freeform Search

Database:	<input type="checkbox"/> US Pre-Grant Publication Full-Text Database <input checked="" type="checkbox"/> US Patents Full-Text Database <input type="checkbox"/> US OCR Full-Text Database <input type="checkbox"/> EPO Abstracts Database <input type="checkbox"/> JPO Abstracts Database <input type="checkbox"/> Derwent World Patents Index <input type="checkbox"/> IBM Technical Disclosure Bulletins
Term:	select\$6 near5 execut\$3 near12 packet\$1 and vliw <div style="display: flex; justify-content: space-around; margin-top: 5px;"> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> </div>
Display:	20 <input type="checkbox"/> Documents in Display Format: <input type="checkbox"/> Starting with Number <input type="checkbox"/>
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

Search History

DATE: Wednesday, April 14, 2004 [Printable Copy](#) [Create Case](#)

<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side			result set
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L10</u>	select\$6 near5 execut\$3 near12 packet\$1 and vliw	10	<u>L10</u>
<u>L9</u>	L8 and (identif\$6 or detect\$3 or determin\$6)	17	<u>L9</u>
<u>L8</u>	L7 and (prefetch\$3 or fetch\$3) and queu\$3	19	<u>L8</u>
<u>L7</u>	vliw near6 packet\$1	80	<u>L7</u>
<u>L6</u>	L4 and (prefetch\$3 or fetch\$3) and queu\$3	6	<u>L6</u>
<u>L5</u>	L4 (prefetch\$3 or fetch\$3) and queu\$3	10809	<u>L5</u>
<u>L4</u>	(vliw) near9 loop\$4	53	<u>L4</u>
<u>L3</u>	L1 and (prefetch\$3 or fetch\$3) and queu\$3	6	<u>L3</u>
<u>L2</u>	L1 and fetch\$3 and queu\$3	6	<u>L2</u>
<u>L1</u>	(vliw) near5 loop\$4	44	<u>L1</u>

END OF SEARCH HISTORY

Hit List

Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs
Generate OACS				

Search Results - Record(s) 1 through 10 of 10 returned.

1. Document ID: US 6658551 B1

Using default format because multiple data bases are involved.

L10: Entry 1 of 10

File: USPT

Dec 2, 2003

US-PAT-NO: 6658551

DOCUMENT-IDENTIFIER: US 6658551 B1

TITLE: Method and apparatus for identifying splittable packets in a multithreaded
VLIW processor

DATE-ISSUED: December 2, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Berenbaum; Alan David	New York City	NY		
Heintze; Nevin	Morristown	NJ		
Jeremiassen; Tor E.	Somerset	NJ		
Kaxiras; Stefanos	Jersey City	NJ		

US-CL-CURRENT: 712/24; 712/215, 717/161

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KOMC	Drawn De
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2. Document ID: US 6182203 B1

L10: Entry 2 of 10

File: USPT

Jan 30, 2001

US-PAT-NO: 6182203

DOCUMENT-IDENTIFIER: US 6182203 B1

TITLE: Microprocessor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KOMC	Drawn De
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3. Document ID: US 6112298 A

L10: Entry 3 of 10

File: USPT

Aug 29, 2000

US-PAT-NO: 6112298

h e b

b g e e e f

e g f ef

b e

DOCUMENT-IDENTIFIER: US 6112298 A

TITLE: Method for managing an instruction execution pipeline during debugging of a data processing system

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn De](#)

4. Document ID: US 6081885 A

L10: Entry 4 of 10

File: USPT

Jun 27, 2000

US-PAT-NO: 6081885

DOCUMENT-IDENTIFIER: US 6081885 A

TITLE: Method and apparatus for halting a processor and providing state visibility on a pipeline phase basis

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn De](#)

5. Document ID: US 6065106 A

L10: Entry 5 of 10

File: USPT

May 16, 2000

US-PAT-NO: 6065106

DOCUMENT-IDENTIFIER: US 6065106 A

TITLE: Resuming normal execution by restoring without refetching instructions in multi-word instruction register interrupted by debug instructions loading and processing

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn De](#)

6. Document ID: US 6055649 A

L10: Entry 6 of 10

File: USPT

Apr 25, 2000

US-PAT-NO: 6055649

DOCUMENT-IDENTIFIER: US 6055649 A

TITLE: Processor test port with scan chains and data streaming

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn De](#)

7. Document ID: US 6016555 A

L10: Entry 7 of 10

File: USPT

Jan 18, 2000

US-PAT-NO: 6016555

DOCUMENT-IDENTIFIER: US 6016555 A

h e b b g e e e f

e g f ef b e

TITLE: Non-intrusive software breakpoints in a processor instruction execution pipeline

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn De](#)

8. Document ID: US 5970241 A

L10: Entry 8 of 10

File: USPT

Oct 19, 1999

US-PAT-NO: 5970241

DOCUMENT-IDENTIFIER: US 5970241 A

TITLE: Maintaining synchronism between a processor pipeline and subsystem pipelines during debugging of a data processing system

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn De](#)

9. Document ID: EP 1148414 A2

L10: Entry 9 of 10

File: EPAB

Oct 24, 2001

PUB-NO: EP001148414A2

DOCUMENT-IDENTIFIER: EP 1148414 A2

TITLE: Method and apparatus for allocating functional units in a multithreaded VLIW processor

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn De](#)

10. Document ID: EP 1102165 A1, JP 2001202241 A

L10: Entry 10 of 10

File: DWPI

May 23, 2001

DERWENT-ACC-NO: 2001-376796

DERWENT-WEEK: 200414

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TITLE: Microprocessor for personal computer, has functional execution units for parallel execution of instructions in execution packet selected from one or more fetch packets

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn De](#)

[Clear](#) | [Generate Collection](#) | [Print](#) | [Fwd Refs](#) | [Bkwd Refs](#) | [Generate OACS](#)

Term	Documents
VLIW	2267
VLIWS	104
SELECT\$6	0

SELECT	610067
SELECTA	150
SELECTAB	14
SELECTABLE	1
SELECTABE	4
SELECTABEL	7
SELECTABI	1
(SELECT\$6 NEAR5 EXECUT\$3 NEAR12 PACKET\$1 AND VLIW).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	10

[There are more results than shown above. Click here to view the entire set.](#)

Display Format: -

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Refine Search

Search Results -

Term	Documents
QUEU\$3	0
QUEU	32
QUEUC	1
QUEUECE	1
QUEUECHE	1
QUEUCY	1
QUEUD	1
QUEUDOT	3
QUEUE	36149
QUEUEAU	1
QUEUEC	1
(L12 AND (QUEU\$3)).PGPB,USPT.	15

[There are more results than shown above. Click here to view the entire set.](#)

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Search History

DATE: Wednesday, April 14, 2004 [Printable Copy](#) [Create Case](#)

Set Name **Query**
side by side

Hit Count **Set Name**
result set

DB=PGPB,USPT; PLUR=YES; OP=OR

L13 L12 and (queu\$3)

15 L13

<u>L12</u>	L11 and l7	58	<u>L12</u>
<u>L11</u>	(712/2-300)![CCLS]	9639	<u>L11</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L10</u>	select\$6 near5 execut\$3 near12 packet\$1 and vliw	10	<u>L10</u>
<u>L9</u>	L8 and (identif\$6 or detect\$3 or determin\$6)	17	<u>L9</u>
<u>L8</u>	L7 and (prefetch\$3 or fetch\$3) and queu\$3	19	<u>L8</u>
<u>L7</u>	vliw near6 packet\$1	80	<u>L7</u>
<u>L6</u>	L4 and (prefetch\$3 or fetch\$3) and queu\$3	6	<u>L6</u>
<u>L5</u>	L4 (prefetch\$3 or fetch\$3) and queu\$3	10809	<u>L5</u>
<u>L4</u>	(vliw) near9 loop\$4	53	<u>L4</u>
<u>L3</u>	L1 and (prefetch\$3 or fetch\$3) and queu\$3	6	<u>L3</u>
<u>L2</u>	L1 and fetch\$3 and queu\$3	6	<u>L2</u>
<u>L1</u>	(vliw) near5 loop\$4	44	<u>L1</u>

END OF SEARCH HISTORY